Suggested Ternary Drive from 3.5V FPGA Outputs.

Two FBGA inputs
Output Levels = 0, 3.5V

TernN → Normally No current input
Both FPGA Inputs = HIGH

Schematic shows all connections to drive a two level Ternary Input to both inputs of a single Ternary Receiver utilizing 4 FBGA outputs.
Vee = -3V

- EACH FPGA OUTPUT ---
- FPGA HI = 3.5V --> No current drawn from ternary input.
- FPGA LOW = 0V --> ~200μA drawn from ternary input.