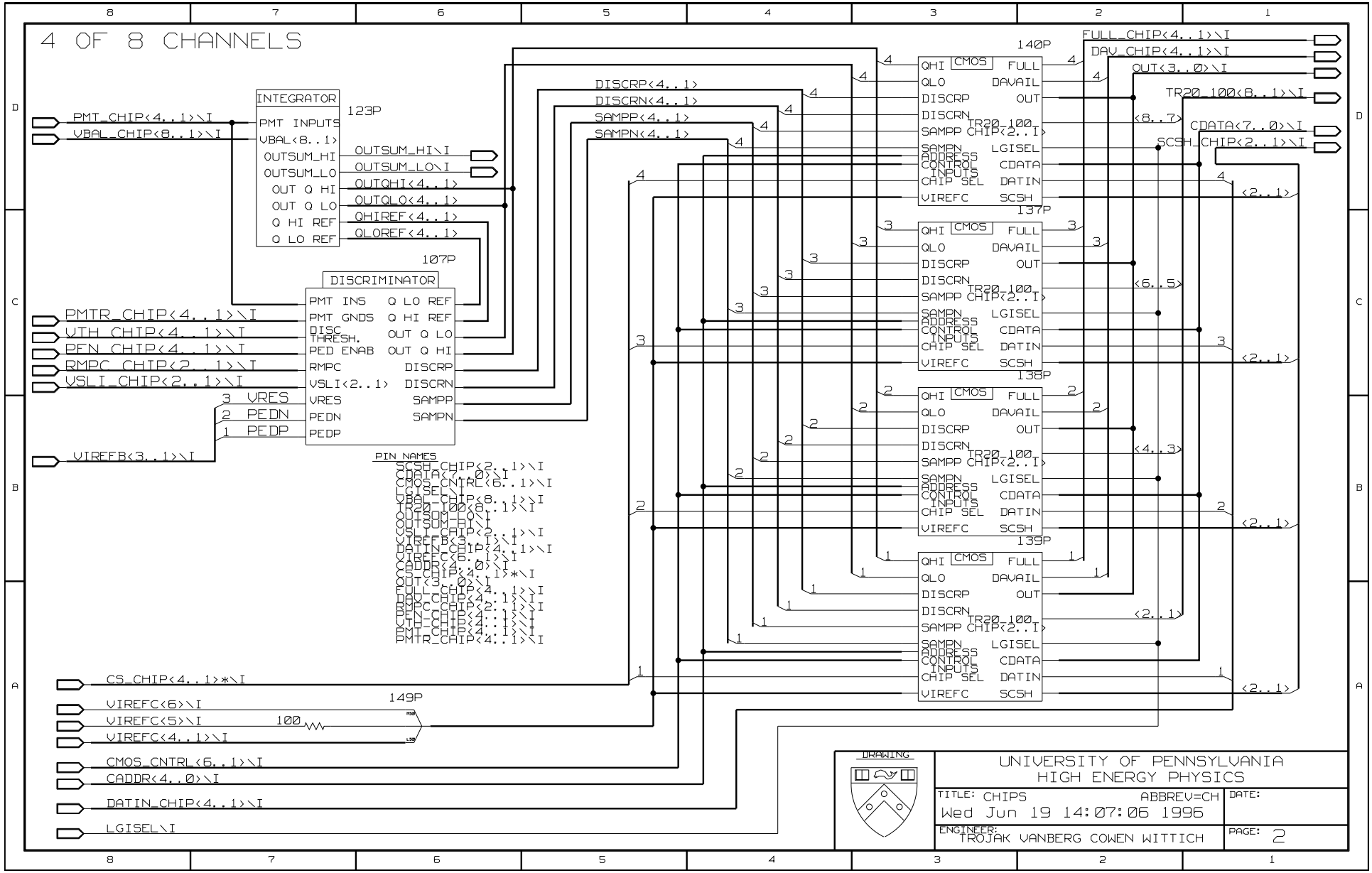
4 OF 8 CHANNELS
INTEGRATOR
123P
PMT INPUTS
VBAL<8..1>
OUTSUM_HI
OUTSUM_LO
OUT Q HI
OUT Q LO
Q HI REF
Q LO REF
PMT_CHIP<4..1>\I
VBAL_CHIP<8..1>\I
OUTSUM_HI\I
OUTSUM_LO\I
OUTQHI<4..1>
OUTQLO<4..1>
QHIREF<4..1>
QLOREF<4..1>
107P
DISCRIMINATOR
PMT INS
PMT GNDS
DISC THRESH.
PED ENAB
RMPC
VSLI<2..1>
VRES
PEDN
PEDP
Q LO REF
Q HI REF
OUT Q LO
OUT Q HI
DISCRP
DISCRN
SAMPP
SAMPN
PMTR_CHIP<4..1>\I
VTH_CHIP<4..1>\I
PEN_CHIP<4..1>\I
RMPC_CHIP<2..1>\I
VSLI_CHIP<2..1>\I
3 VRES
2 PEDN
1 PEDP
VIREFB<3..1>\I
DISCRP<4..1>
DISCRN<4..1>
SAMPP<4..1>
SAMPN<4..1>
PIN NAMES
SCSH_CHIP<2..1>\I
CDATA<7..0>\I
CMOS_CNTRL<6..1>\I
LGISEL\I
VBAL_CHIP<8..1>\I
TR20_100<8..1>\I
OUTSUM_HI\I
OUTSUM_LO\I
VSLI_CHIP<2..1>\I
VIREFB<3..1>\I
DATIN_CHIP<4..1>\I
VIREFC<6..1>\I
CADDR<4..0>\I
CS_CHIP<4..1>*\I
OUT<3..0>\I
TCLK_CHIP<4..1>\I
DAV_CHIP<4..1>\I
RMPC_CHIP<2..1>\I
CTH_CHIP<4..1>\I
PEN_CHIP<4..1>\I
PMT_CHIP<4..1>\I
PMTR_CHIP<4..1>\I
140P
137P
138P
139P
QHI
QLO
DISCRP
DISCRN
SAMPP
SAMPN
VIREFC
CMOS
FULL
DAVAIL
OUT
TR20_100
CHIP SEL
INPUTS
ADDRESS
CONTROL
LGISEL
CDATA
DATIN
SCSH
FULL_CHIP<4..1>\I
DAV_CHIP<4..1>\I
OUT<3..0>\I
TR20_100<8..1>\I
CDATA<7..0>\I
SCSH_CHIP<2..1>\I
<8..7>
<6..5>
<4..3>
<2..1>
CS_CHIP<4..1>*\I
149P
100
VIREFC<6>\I
VIREFC<5>\I
VIREFC<4..1>\I
CMOS_CNTRL<6..1>\I
CADDR<4..0>\I
DATIN_CHIP<4..1>\I
LGISEL\I
DRAWING
UNIVERSITY OF PENNSYLVANIA
HIGH ENERGY PHYSICS
TITLE: CHIPS
ABBREV=CH
Wed Jun 19 14:07:06 1996
DATE:
ENGINEER: TROJAK VANBERG COWEN WITTICH
PAGE: 2