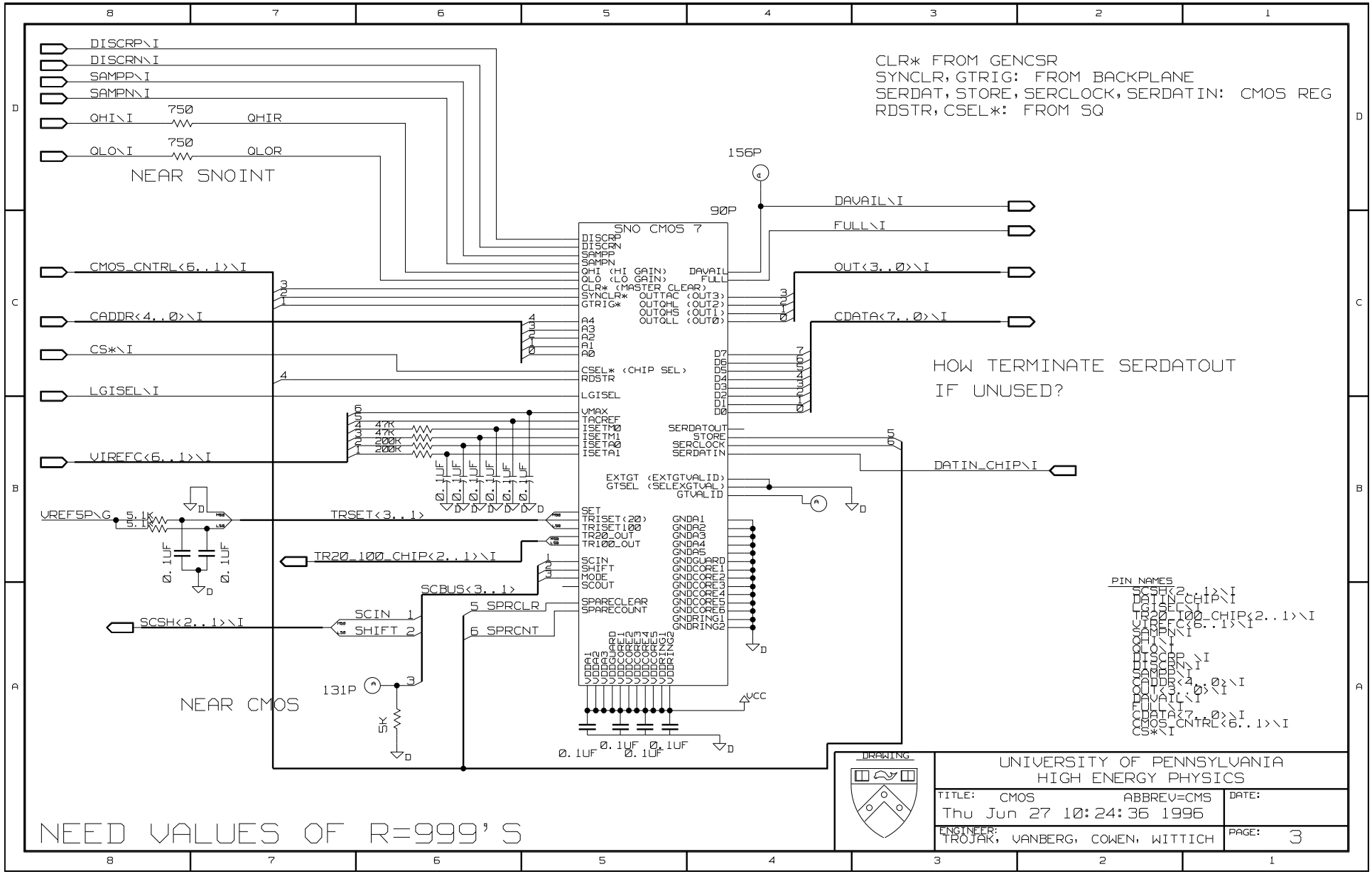
CMOS ASIC


CLR* FROM GENCSR
SYNCLR, GTRIG: FROM BACKPLANE
SERDAT, STORE, SERCLOCK, SERDATIN: CMOS REG
RDSTR, CSEL*: FROM SQ
DISCRP\I
DISCRN\I
SAMPP\I
SAMPN\I
QHI\I
QLO\I
750
QHIR
QLOR
NEAR SNOINT
156P
90P
SNO CMOS 7
DAVAIL\I
FULL\I
OUT<3..0>\I
CDATA<7..0>\I
CMOS_CNTRL<6..1>\I
CADDR<4..0>\I
CS*\I
LGISEL\I
VIREFC<6..1>\I
HOW TERMINATE SERDATOUT
IF UNUSED?
DATIN_CHIP\I
VREF5P\G
5.1K
TRSET<3..1>
TR20_100_CHIP<2..1>\I
SCBUS<3..1>
SCSH<2..1>\I
SCIN 1
SHIFT 2
5 SPRCLR
6 SPRCNT
131P
3
5K
NEAR CMOS
0.1UF
47K
200K
VCC
PIN NAMES
SCSH<2..1>\I
DATIN_CHIP\I
TR20_100_CHIP<2..1>\I
VIREFC<6..1>\I
SAMPN\I
QHI\I
QLO\I
SAMPP\I
DISCRP\I
DISCRN\I
CADDR<4..0>\I
OUT<3..0>\I
DAVAIL\I
FULL\I
CDATA<7..0>\I
CMOS_CNTRL<6..1>\I
CS*\I
NEED VALUES OF R=999'S
DRAWING
UNIVERSITY OF PENNSYLVANIA
HIGH ENERGY PHYSICS
TITLE: CMOS
ABBREV=CMS
Thu Jun 27 10:24:36 1996
DATE:
ENGINEER: TROJAK, VANBERG, COWEN, WITTICH
PAGE: 3