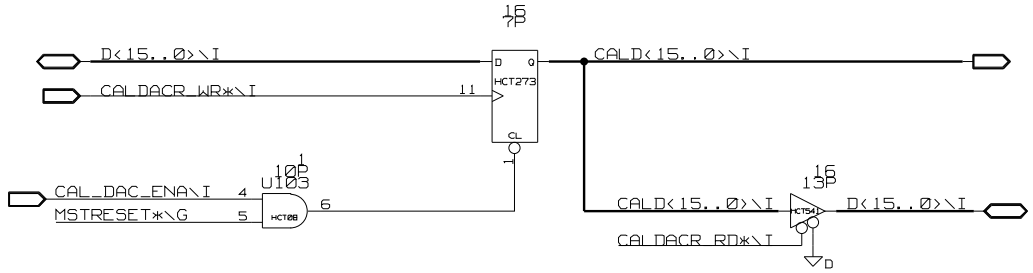
CALIBRATION DAC REG.



PIN NAMES
CALDACR-RD*\I
CALDACR-WR*\I
CAL_DAC-ENA\I
CALD<15..0>\I
D<31..0>\I

CAL_DAC_ENA	MSTRESET*	CL*
0	0	0 (ALWAYS CLEARED)
0	1	0 (ALWAYS CLEARED)
1	0	0 (ALWAYS CLEARED)
1	1	1 (WRITE CALDAC)

IF CAL_DAC_ENA = 1 *AND* MSTRESET* = 1
ONLY THEN, DO NOT CLEAR REGISTER

DRAWING

UNIVERSITY OF PENNSYLVANIA
HIGH ENERGY PHYSICS

TITLE: CALDACR ABBREV=CALDACR
Mon Feb 24 15:17:40 1997

DATE:

ENGINEER: TROJAK, VANBERG, COWEN, WITTICH

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