CENTRAL BACKPLANE CONNECTOR

SNOBUS DRAFT PIN ARRANGEMENT - FEC STATION

REVISED VERSION WITH:
NO MORE THAN 4 BUS LINES PER ROW
VMB ON C4, C5 PINS
SEPARATE ROWS FOR VP24, VM24
ADJUSTED TO ALLOW FOR PADDLE CARD ORIENTATION

32 CHAN FEC CARD VIEW
TRANSLATOR TERMINATES THE TRIGGER AND TIMING BUSSES
TRANSLATOR DOES HAVE 17 SELECT LINES
NAMING: TOP SIGNALS END IN T, BOTTOM IN B
SB PREFIX = SNOBUS - BACKPLANE SIGNALS

CONN165

A		B		C		D		E	
A1	1 GND	B1	34 SBNHIT20B	C1	67 GND	D1	100 SBNHIT100B	E1	133 CONTY1\I
A2	2 SBESUMLB	B2	35 GND	C2	68 SBESUMHB	D2	101 GND	E2	134 GND
A3	3 GND	B3	36 GND	C3	69 GND	D3	102 GND	E3	135 GND
A4	4 SBGLTRH	B4	37 SBGLTRL	C4	70 VMB	D4	103 SBTESTTH	E4	136 SBTESTTL
A5	5 SBSYNCLRH	B5	38 SBSYNCLRL	C5	71 VMB	D5	104 SBSYNCLR24H	E5	137 SBSYNCLR24L
A6	6 GND	B6	39 SBGEO<0>\I	C6	72 SBGEO<1>\I	D6	105 SBGEO<2>\I	E6	138 SBGEO<3>\I
A7	7 GND	B7	40 SBA<2>\I	C7	73 SBA<3>\I	D7	106 SBA<4>\I	E7	139 SBA<5>\I
A8	8 SBA<6>\I	B8	41 GND	C8	74 SBA<7>\I	D8	107 SBA<8>\I	E8	140 SBA<9>\I
A9	9 SBA<10>\I	B9	42 SBA<11>\I	C9	75 GND	D9	108 SBA<12>\I	E9	141 SBA<13>\I
A10	10 SBA<14>\I	B10	43 SBA<15>\I	C10	76 SBA<16>\I	D10	109 GND	E10	142 SBA<17>\I
A11	11 SBA<18>\I	B11	44 SBA<19>\I	C11	77 SBA<20>\I	D11	110 SBA<21>\I	E11	143 GND
A12	12 GND	B12	45 SBD<0>\I	C12	78 SBD<1>\I	D12	111 SBD<2>\I	E12	144 SBD<3>\I
A13	13 SBD<4>\I	B13	46 SBD<5>\I	C13	79 SBD<6>\I	D13	112 GND	E13	145 SBD<7>\I
A14	14 SBD<8>\I	B14	47 SBD<9>\I	C14	80 GND	D14	113 SBD<10>\I	E14	146 SBD<11>\I
A15	15 SBD<12>\I	B15	48 GND	C15	81 SBD<13>\I	D15	114 SBD<14>\I	E15	147 SBD<15>\I
A16	16 GND	B16	49 SBD<16>\I	C16	82 SBD<17>\I	D16	115 SBD<18>\I	E16	148 SBD<19>\I
A17	17 SBD<20>\I	B17	50 SBD<21>\I	C17	83 SBD<22>\I	D17	116 SBD<23>\I	E17	149 GND
A18	18 SBD<24>\I	B18	51 SBD<25>\I	C18	84 SBD<26>\I	D18	117 GND	E18	150 SBD<27>\I
A19	19 SBD<28>\I	B19	52 SBD<29>\I	C19	85 GND	D19	118 SBD<30>\I	E19	151 SBD<31>\I
A20	20 SBREGSPACE	B20	53 GND	C20	86 VM24	D20	119 VM24	E20	152 SBRESET
A21	21 GND	B21	54 SBDTACK	C21	87 SBWRITE	D21	120 VGTLREF\I	E21	153 SBDAVA\I
A22	22 VP24	B22	55 VP24	C22	88 GND	D22	121 GND	E22	154 SBDONE_PROGXL
A23	23 SBDINXL	B23	56 GND	C23	89 SBCLKXL	D23	122 SBSELECT\I	E23	155 GND
A24	24 VP8	B24	57 VP8	C24	90 VP8	D24	123 VP8	E24	156 VP8
A25	25 GND	B25	58 GND	C25	91 GND	D25	124 GND	E25	157 GND
A26	26 VP5	B26	59 VP5	C26	92 VP5	D26	125 VP5	E26	158 VP5
A27	27 VP5	B27	60 VP5	C27	93 VP5	D27	126 VP5	E27	159 VP5
A28	28 SBCLKADH	B28	61 SBCLKADL	C28	94 GND	D28	127 SBCLKMEMH	E28	160 SBCLKMEML
A29	29 SBCLKSQH	B29	62 SBCLKSQL	C29	95 GND	D29	128 ITRIGREF20\I	E29	161 ITRIGREF100\I
A30	30 VM5	B30	63 VM5	C30	96 VM5	D30	129 VM5	E30	162 VM5
A31	31 VM5	B31	64 VM5	C31	97 VM5	D31	130 VM5	E31	163 VM5
A32	32 SBESUMLT	B32	65 GND	C32	98 SBESUMHT	D32	131 GND	E32	164 GND
A33	33 GND	B33	66 SBNHIT20T	C33	99 GND	D33	132 SBNHIT100T	E33	165 CONTY2\I

J17
54P

HIGH/LOW TRUE FINAL OR PENULTIMATE H OR L

BOTTOM TRIGGER
GND LINE
TIMING
GEOGRAPHIC ADDRESS
BUS ADDRESS
BUS DATA
BUS CONTROL
XILINX LOAD PINS
MAIN POWER DIST
CLOCK
GND LINE
TOP TRIGGER

CS4 0.1UF

48P
SBREGSPACE
SBWRITE
SBDTACK
SBRESET
SBBUSCONTROL<4..1>\I

49P
SBSYNCLRH
SBSYNCLRL
SBGLTRH
SBGLTRL
SBTESTTH
SBTESTTL
SBSYNCLR24H
SBSYNCLR24L
SBTIMEBUS<8..1>\I

60P
SBCLKXL
SBDINXL
SBDONE_PROGXL
SBXLINX<3..1>\I

57P
GND
VP5
VM5
VP8
VM8
VP24
VM24
POWER<7..1>\I

53P
SBCLKADH
SBCLKADL
SBCLKSQH
SBCLKSQL
SBCLKMEMH
SBCLKMEML
SBCLOCKS<6..1>\I

56P
SBESUMLT
SBESUMHT
SBESUMLB
SBESUMHB
SBETRIG<4..1>\I

SBNHIT20T 58P
SBNHIT100T
SBNHITTOP<2..1>\I

SBNHIT20B 59P
SBNHIT100B
SBNHITBOT<2..1>\I

61P SB_FEC<165..1>\I

THREE CLOCK VERSION -
SBCLKAD FOR ADCS
SBCKLSQ FOR SEQUENCER
SBCLKMEM FOR MEMORY

CONTY1 AND CONTY2 ARE CONNECTOR CONTINUITY TEST PINS

POWER IS:
+24V, -24V - ADCS, SOME OP AMPS/DACS, REFERENCES, TRIGGER
+8 FOR BIPOLAR, DACS, -8 FOR DACS
+5 FOR CMOS AND GENERAL LOGIC
-5.2 FOR ECL AND BIPOLAR

REFERENCES ARE:
VTRIGREF FOR CURRENT SUM BASE REFERENCES
VGTLREF FOR GTL TRANSCEIVER TERMINATIONS

DRAWING

UNIVERSITY OF PENNSYLVANIA
HIGH ENERGY PHYSICS

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