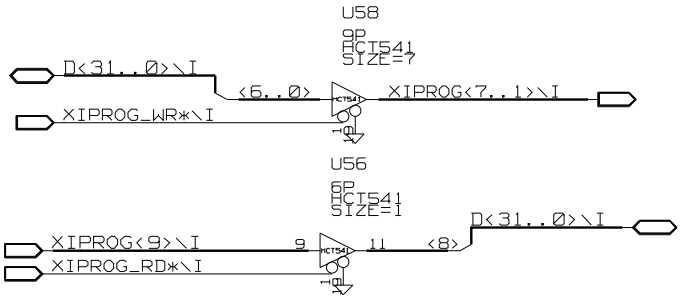


# HOW TO PROGRAM THE XILINX CHIPS

| DATA BUS BIT | SIGNAL NAME | XIPROG BUS BIT |
|---|---|---|
| 0 | CLOCK | 1 |
| 1-4 | PROGRAM* | 2-5 |
| 1 | SEQ | 2 |
| 2 | FIFO | 3 |
| 3 | CGT | 4 |
| 5 | PWRDWN* | 6 |
| 6 | RTRIG/M0 | 7 |
| 7 | RESET* | 8 |
| 8 | RDATA*/M1 | 9 |

PIN NAMES
XIPROG_WR*\I
XIPROG_RD*\I
XIPROG<7..1>\I
D<31..0>\I

DRAWING

UNIVERSITY OF PENNSYLVANIA
HIGH ENERGY PHYSICS

TITLE: XIPROG_REG ABBREV=XIR
Mon Oct 16 17:17:18 1995

DATE:

ENGINEER: COWEN

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