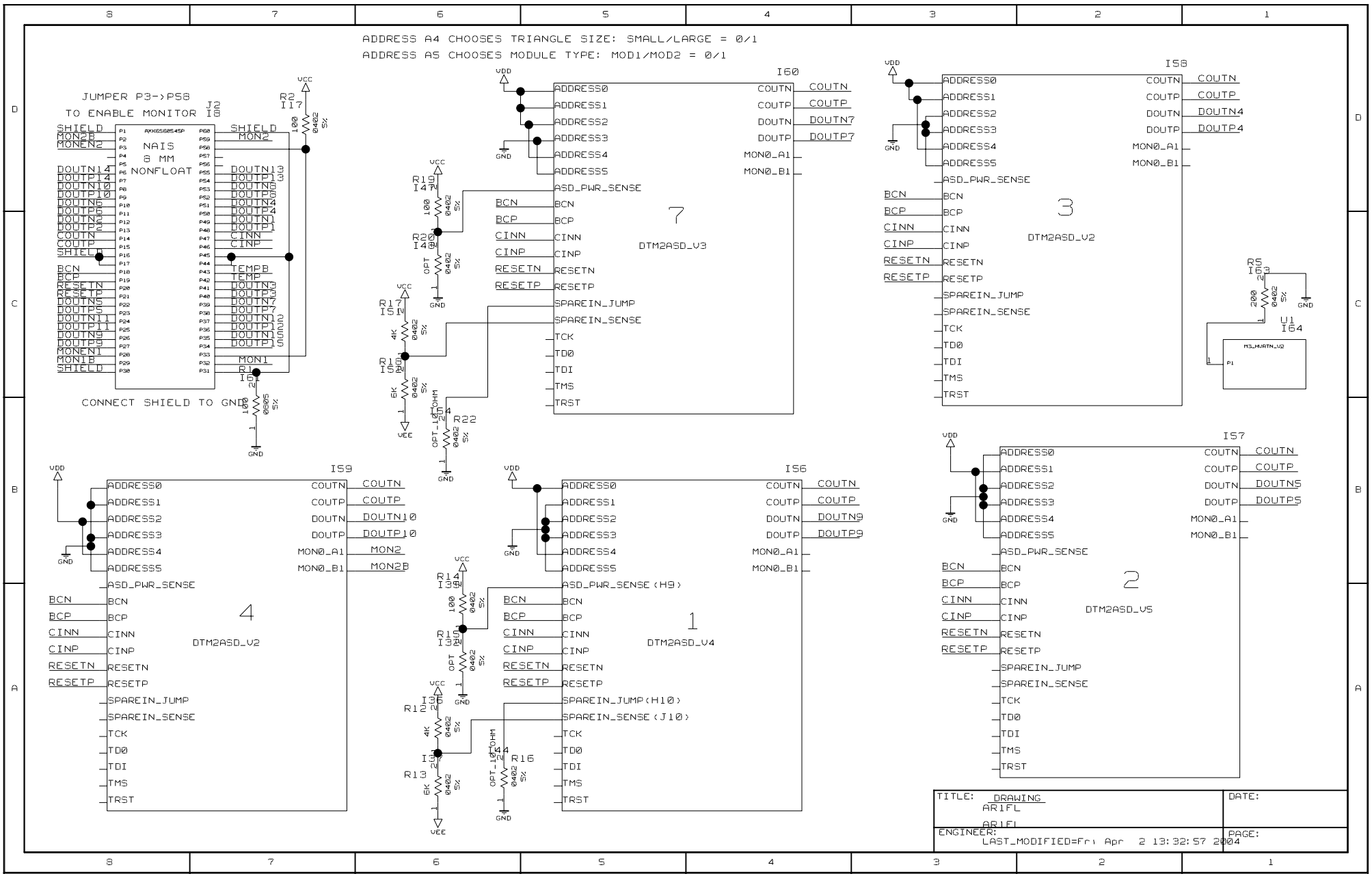
ADDRESS A4 CHOOSES TRIANGLE SIZE: SMALL/LARGE = 0/1
ADDRESS A5 CHOOSES MODULE TYPE: MOD1/MOD2 = 0/1
JUMPER P3->P58
TO ENABLE MONITOR
CONNECT SHIELD TO GND
NAIS
8 MM
NONFLOAT
I60
DTM2ASD_V3
I58
DTM2ASD_V2
I59
DTM2ASD_V2
I56
DTM2ASD_V4
I57
DTM2ASD_V5
ADDRESS0
ADDRESS1
ADDRESS2
ADDRESS3
ADDRESS4
ADDRESS5
ASD_PWR_SENSE
BCN
BCP
CINN
CINP
RESETN
RESETP
SPAREIN_JUMP
SPAREIN_SENSE
TCK
TDO
TDI
TMS
TRST
COUTN
COUTP
DOUTN
DOUTP
MON0_A1
MON0_B1
TITLE: DRAWING
AR1FL
DATE:
ENGINEER:
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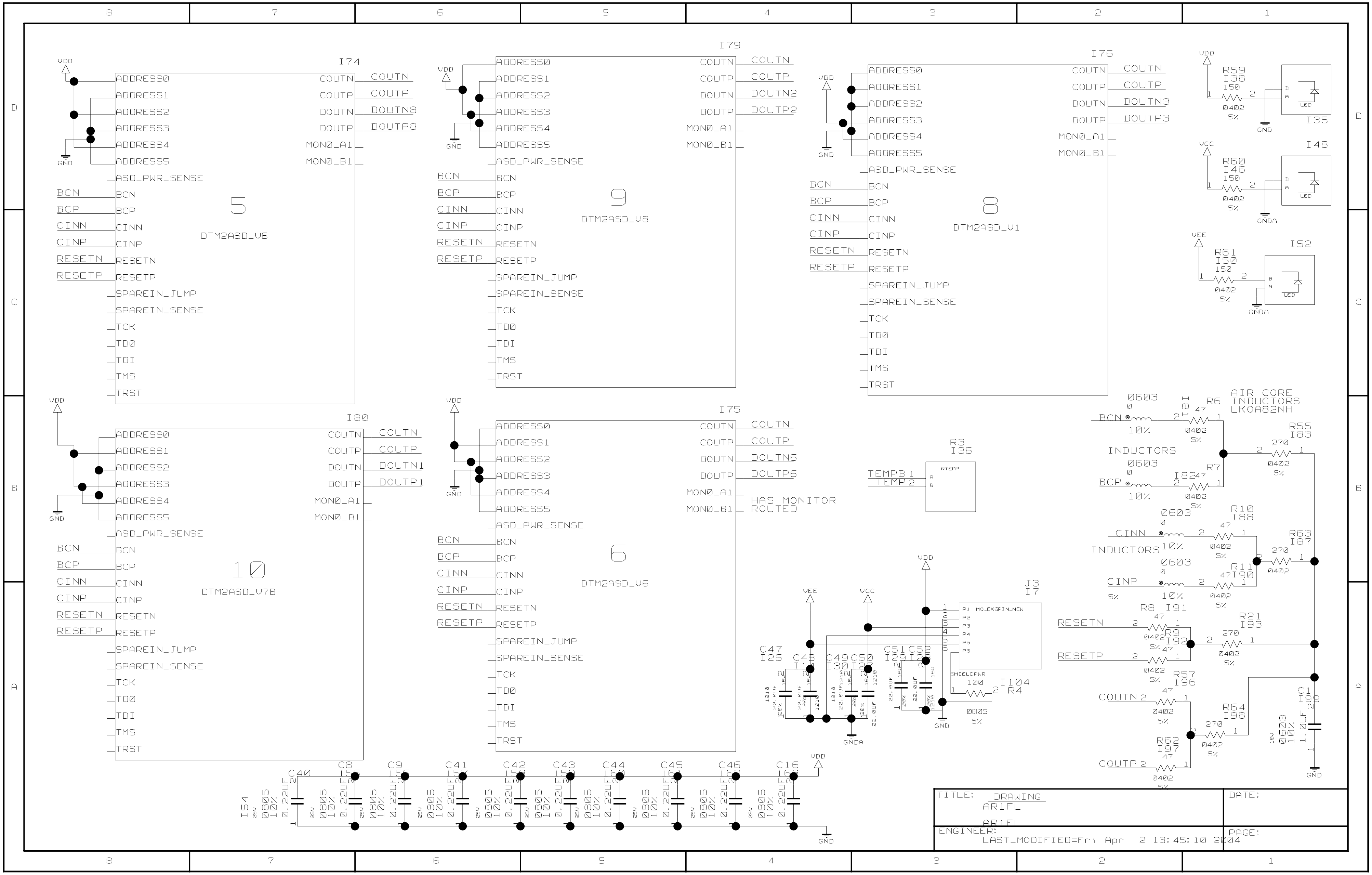
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DTM2ASD_V8
DTM2ASD_V1
DTM2ASD_V7B
DTM2ASD_V6
I74
I79
I76
I80
I75
ADDRESS0
ADDRESS1
ADDRESS2
ADDRESS3
ADDRESS4
ADDRESS5
ASD_PWR_SENSE
BCN
BCP
CINN
CINP
RESETN
RESETP
SPAREIN_JUMP
SPAREIN_SENSE
TCK
TD0
TDI
TMS
TRST
COUTN
COUTP
DOUTN
DOUTP
MON0_A1
MON0_B1
DOUTN8
DOUTP8
DOUTN2
DOUTP2
DOUTN3
DOUTP3
DOUTN1
DOUTP1
DOUTN6
DOUTP6
HAS MONITOR
ROUTED
VDD
VCC
VEE
GND
GNDA
AIR CORE
INDUCTORS
LKOA62NH
INDUCTORS
TEMPB 1
TEMP 2
RTEMP
MOLEX6PIN_NEW
SHIELDPWR
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COUTP 2
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AR1FL
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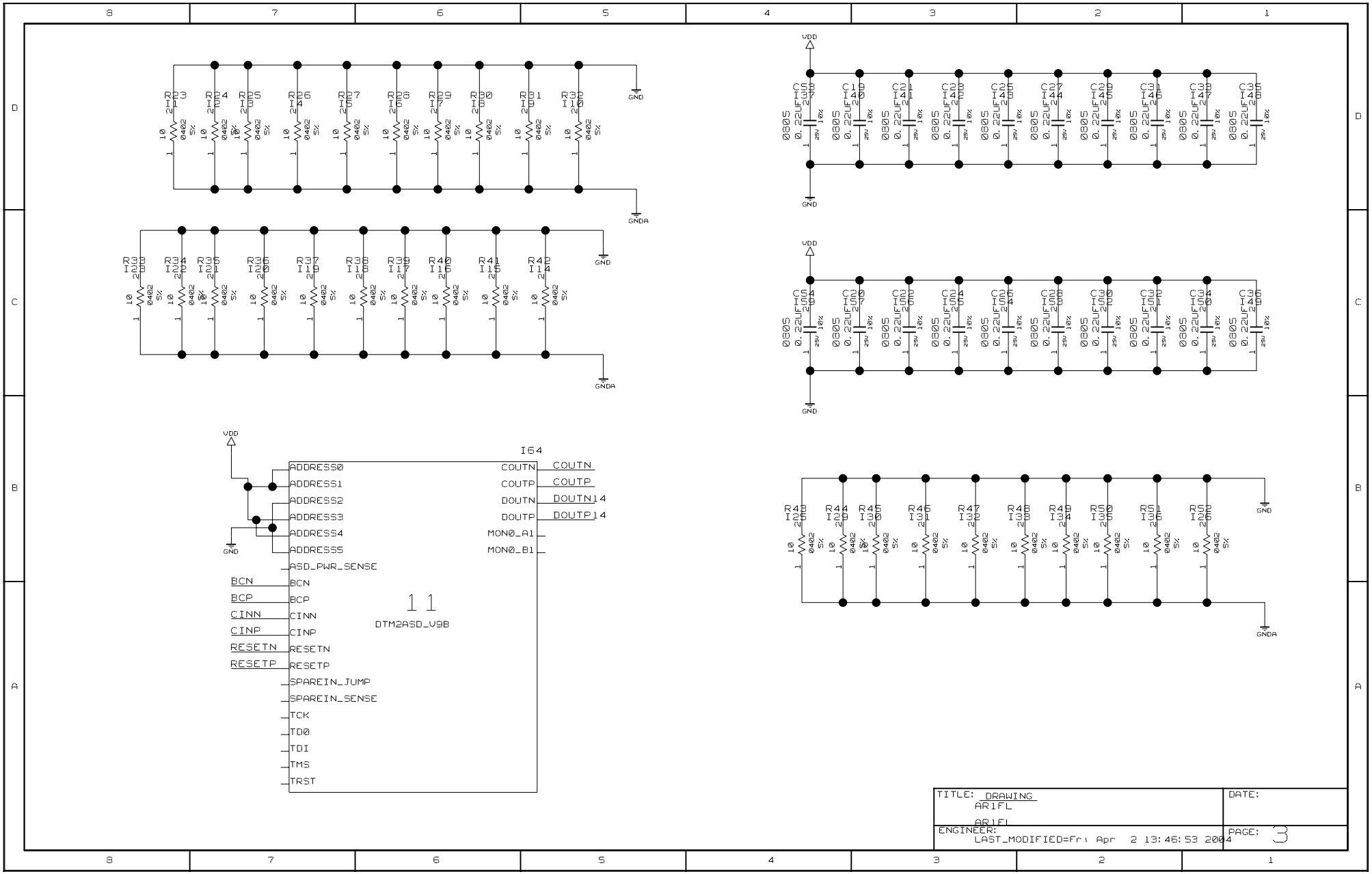
VDD
GND
GNDA
I64
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ADDRESS1
ADDRESS2
ADDRESS3
ADDRESS4
ADDRESS5
ASD_PWR_SENSE
BCN
BCP
CINN
CINP
RESETN
RESETP
SPAREIN_JUMP
SPAREIN_SENSE
TCK
TDO
TDI
TMS
TRST
COUTN
COUTP
DOUTN
DOUTP
MON0_A1
MON0_B1
COUTN
COUTP
DOUTN14
DOUTP14
11
DTM2ASD_V9B
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AR1FL
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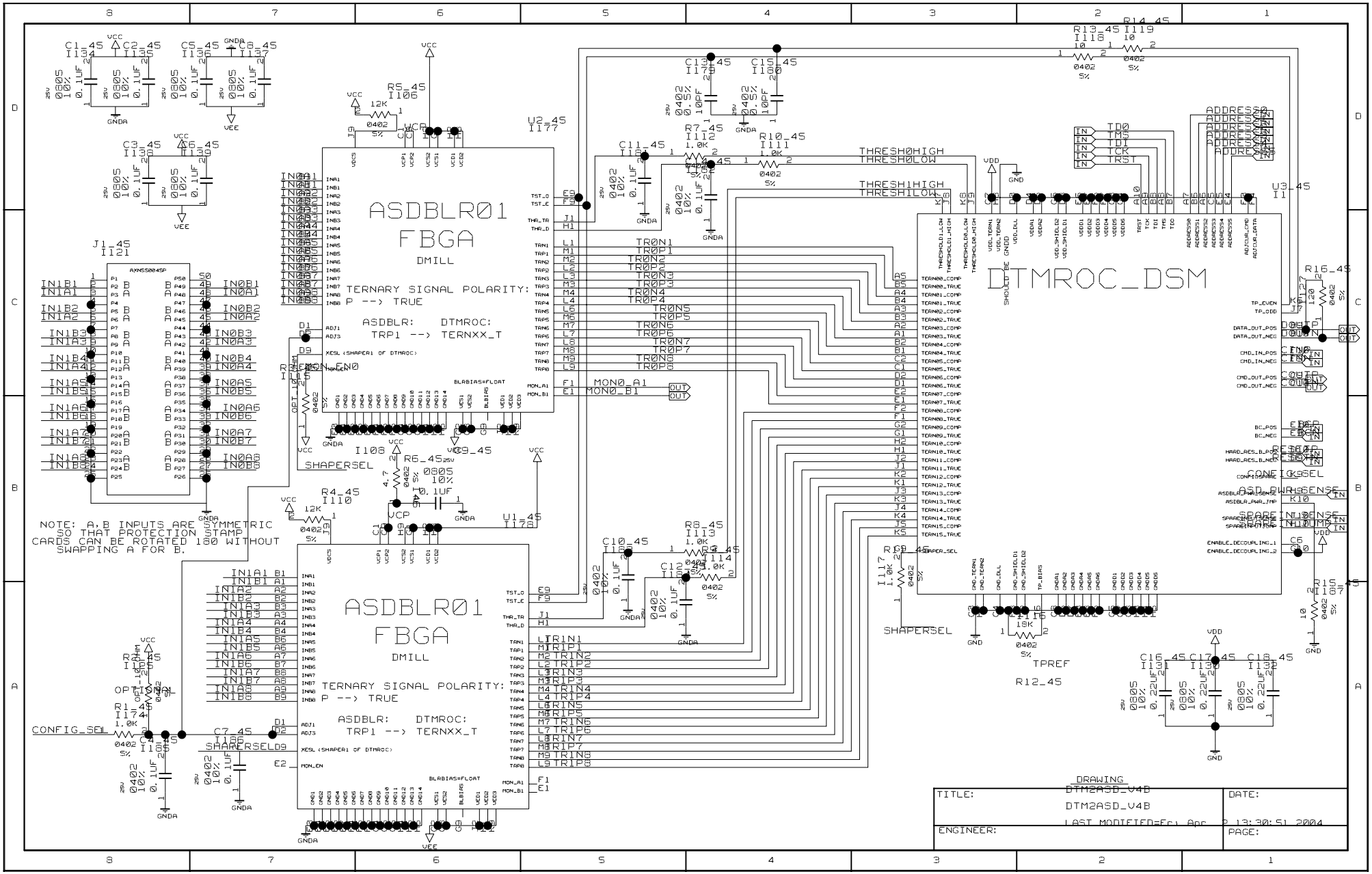
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DMILL
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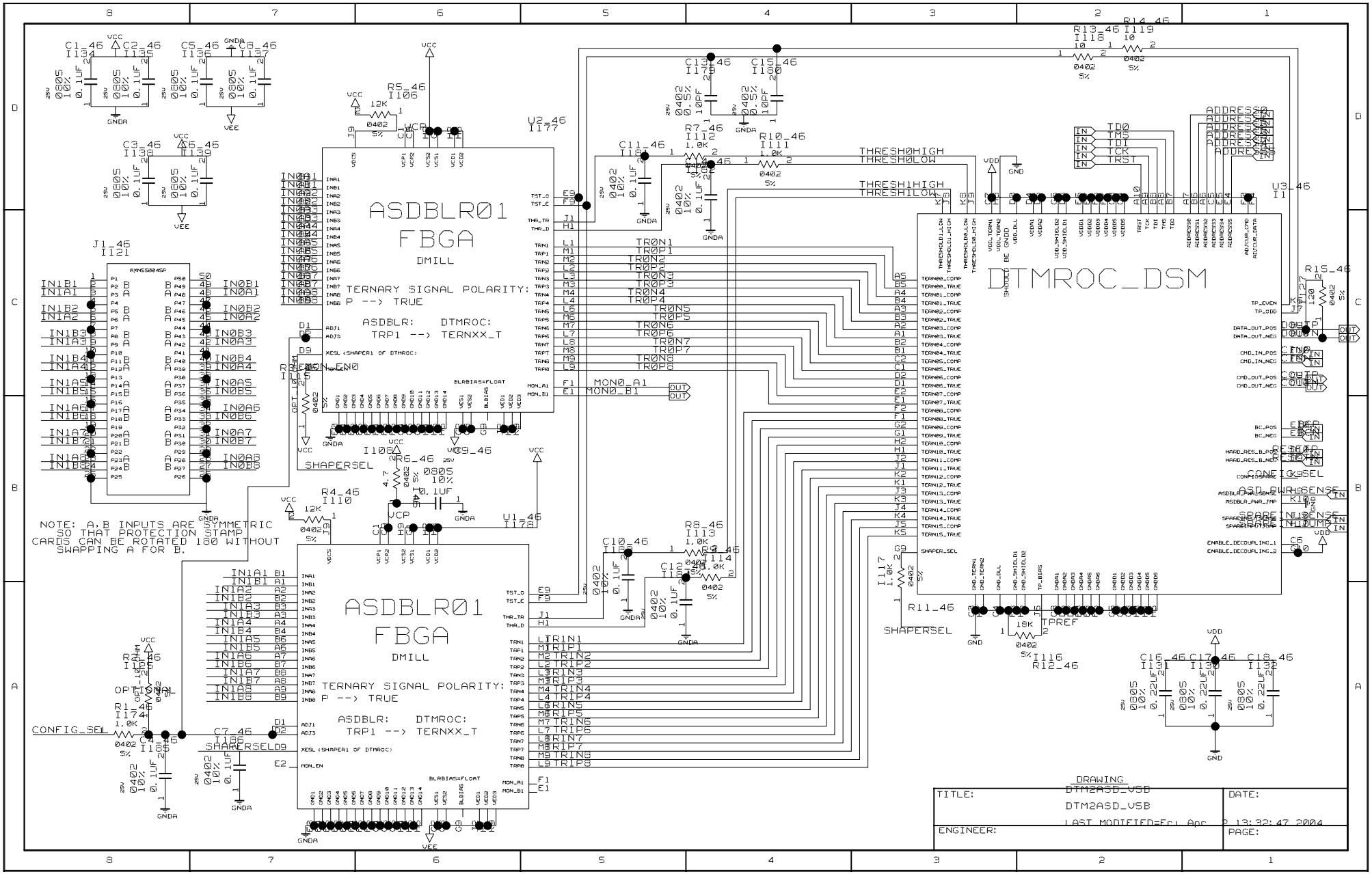
P --> TRUE
ASDBLR: DTMROC:
TRP1 --> TERNXX_T
DTMROC_DSM
SHAPERSEL
CONFIG_SEL
NOTE: A,B INPUTS ARE SYMMETRIC SO THAT PROTECTION STAMP CARDS CAN BE ROTATED 180 WITHOUT SWAPPING A FOR B.
DRAWING
DTM2ASD_V4B
TITLE:
DTM2ASD_V4B
DATE:
ENGINEER:
PAGE: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NOTE: A,B INPUTS ARE SYMMETRIC SO THAT PROTECTION STAMP CARDS CAN BE ROTATED 180 WITHOUT SWAPPING A FOR B.
SHAPERSEL
CONFIG_SEL
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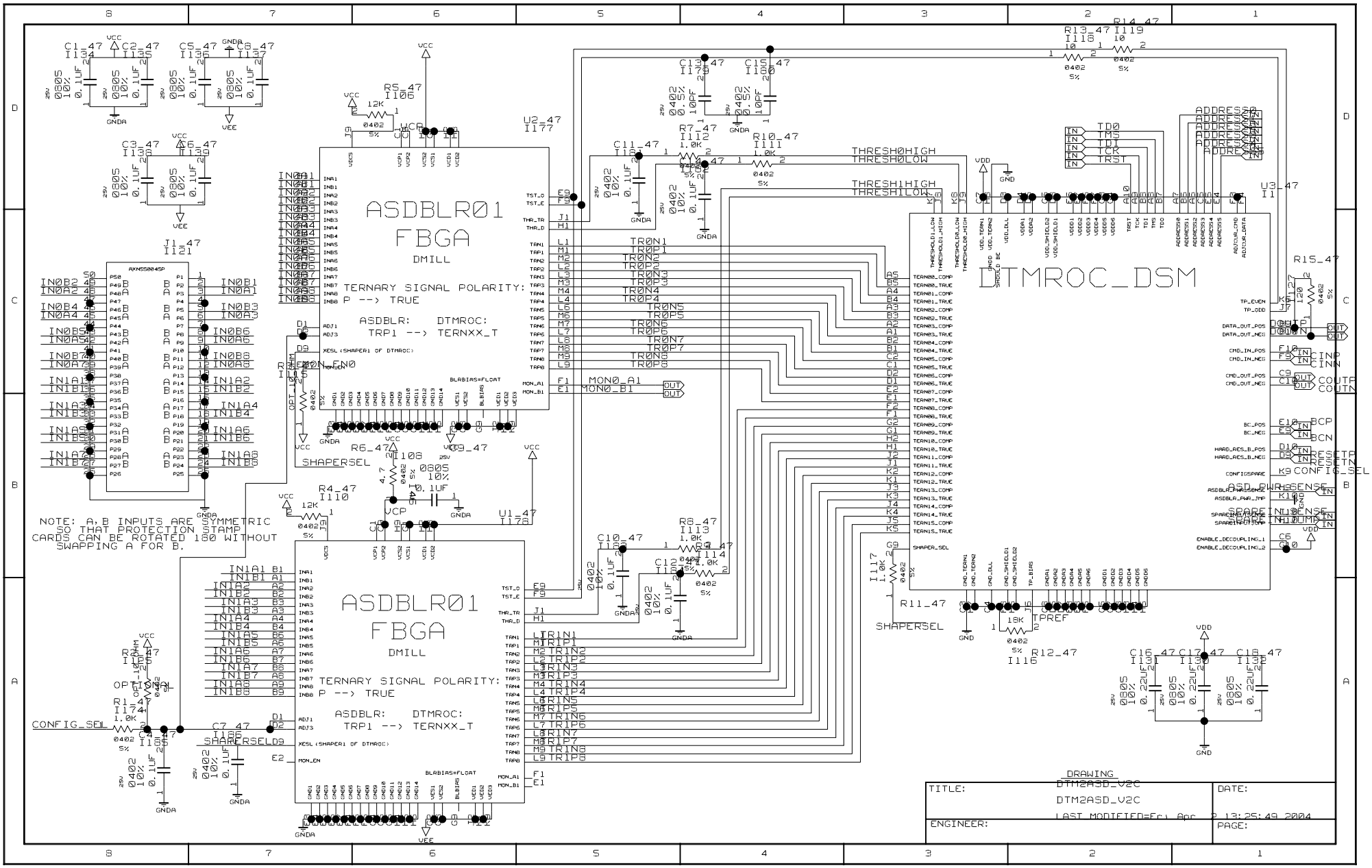
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DMILL
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ASDBLR: DTMROC:
TRP1 --> TERNXX_T
DTMROC_DSM
SHAPERSEL
CONFIG_SEL
NOTE: A,B INPUTS ARE SYMMETRIC SO THAT PROTECTION STAMP CARDS CAN BE ROTATED 180 WITHOUT SWAPPING A FOR B.
TITLE:
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DRAWING
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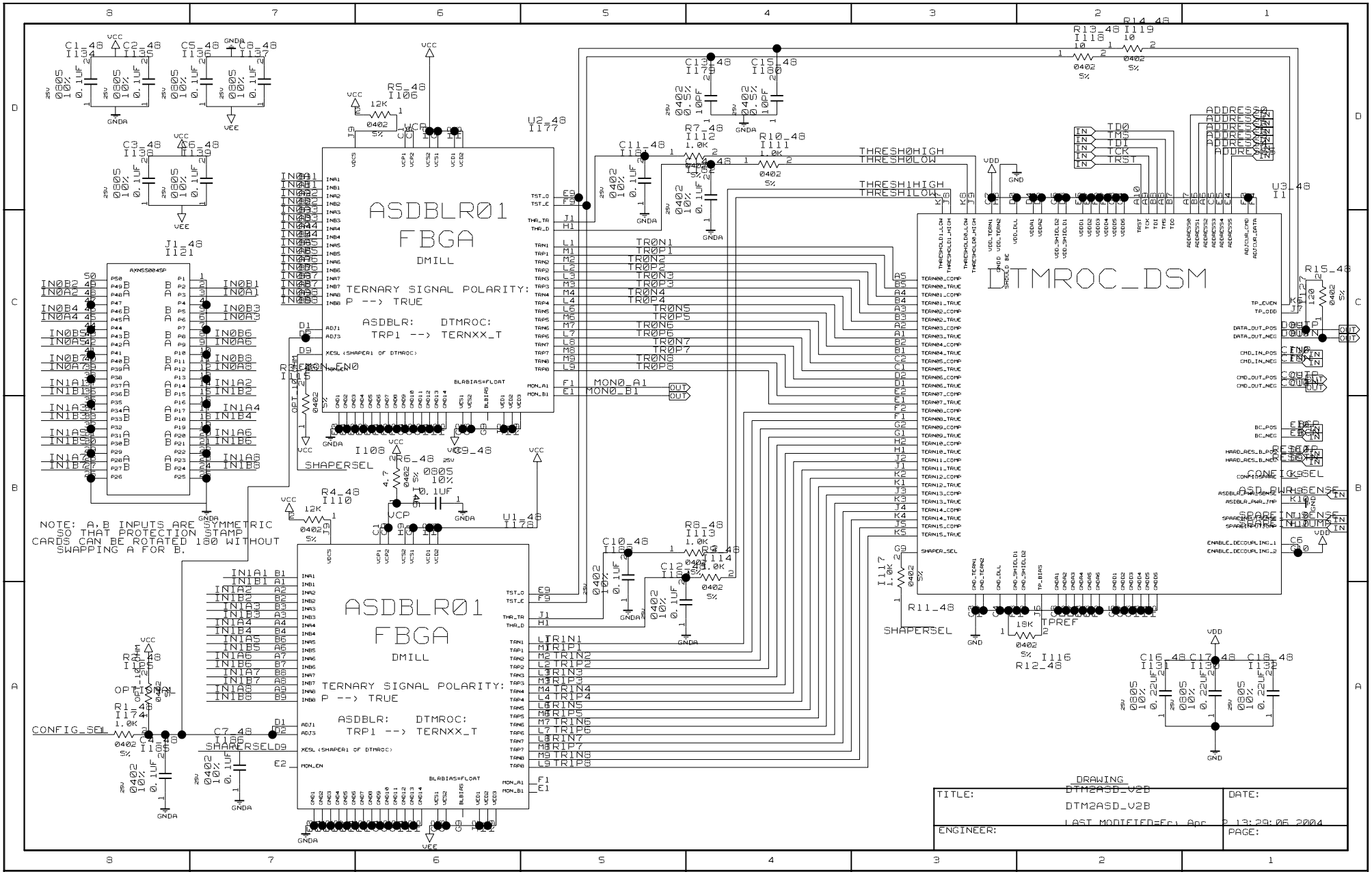
DATE:
ENGINEER:
PAGE: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FBGA
DMILL
TERNARY SIGNAL POLARITY:
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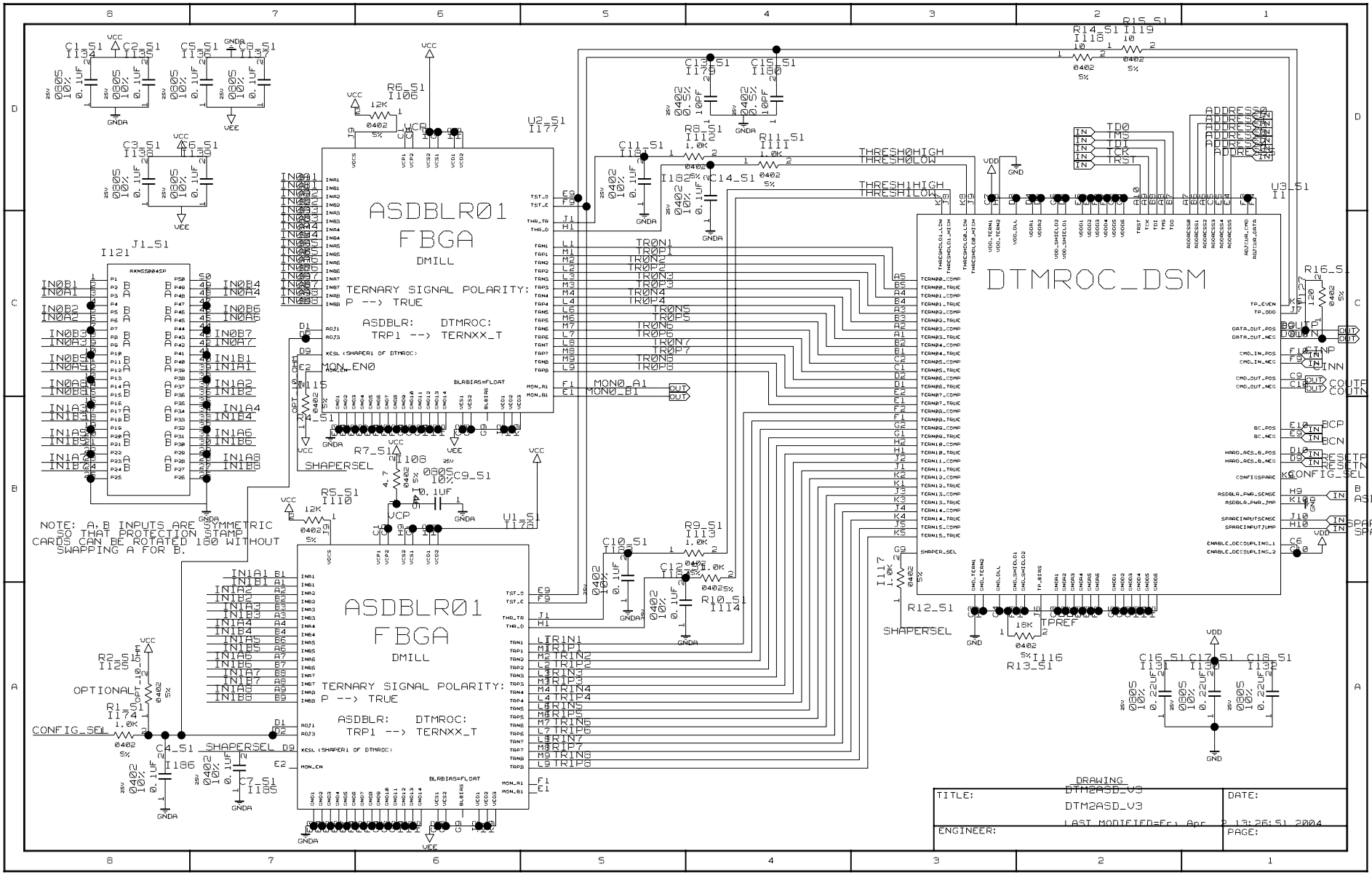
ASDBLR: DTMROC:
TRP1 --> TERNXX_T
DTMROC_DSM
NOTE: A,B INPUTS ARE SYMMETRIC SO THAT PROTECTION STAMP CARDS CAN BE ROTATED 180 WITHOUT SWAPPING A FOR B.
DRAWING
DTM2ASD_V2B
TITLE:
DTM2ASD_V2B
DATE:
ENGINEER:
PAGE:


ASDBLR01
FBGA
DMILL
TERNARY SIGNAL POLARITY:
P --> TRUE
ASDBLR: DTMROC:
TRP1 --> TERNXX_T
DTMROC_DSM
NOTE: A,B INPUTS ARE SYMMETRIC
SO THAT PROTECTION STAMP
CARDS CAN BE ROTATED 180 WITHOUT
SWAPPING A FOR B.
OPTIONAL
CONFIG_SEL
SHAPERSEL
ASD_PWR_SENSE
SPAREIN_SENSE
SPAREIN_JUMP
TITLE:
DTM2ASD_V3
DRAWING
DTM2ASD_V3
DATE:
LAST MODIFIED=Fri Apr 2 13:26:51 2004
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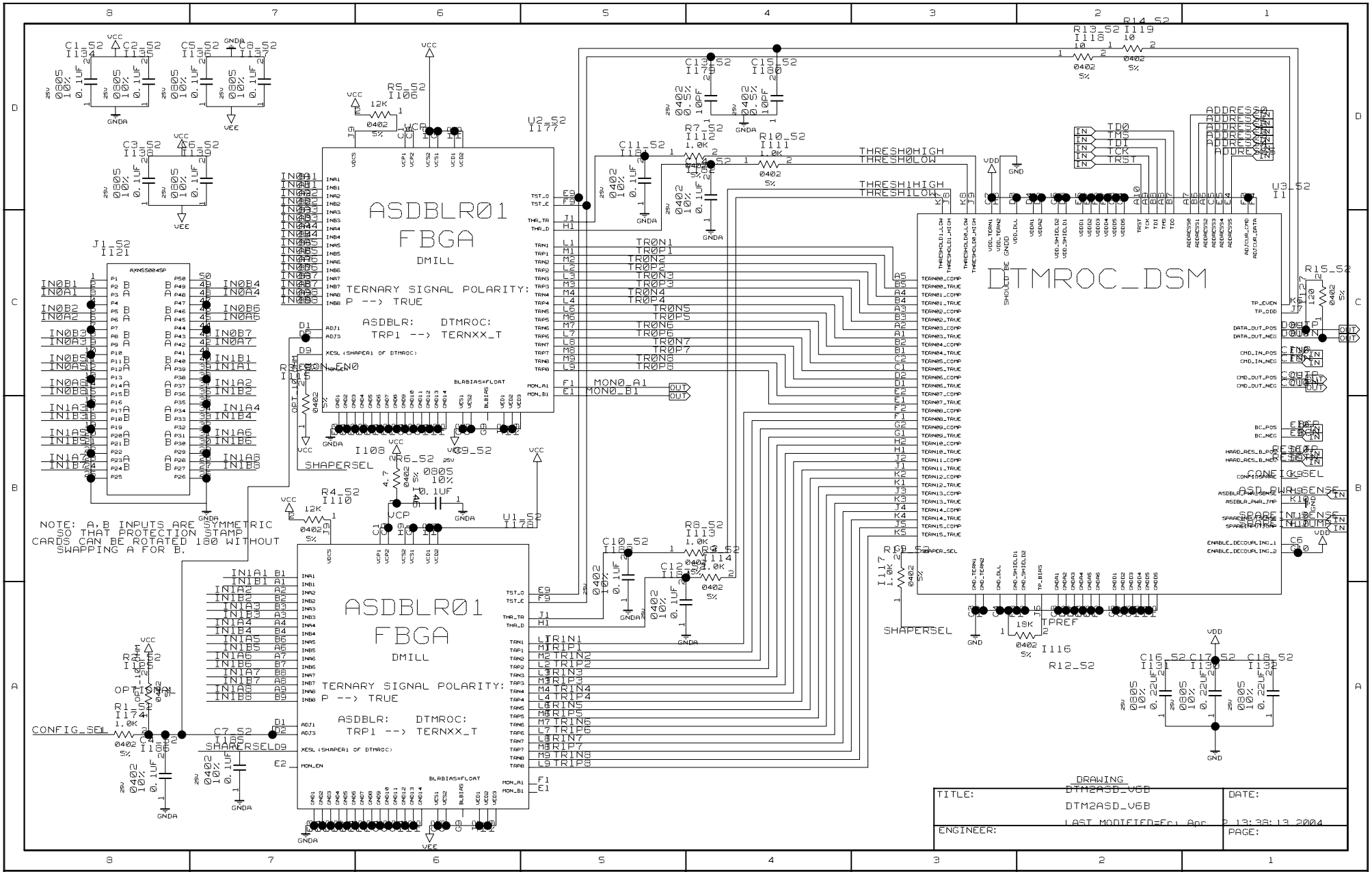
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FBGA
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TRP1 --> TERNXX_T
DTMROC_DSM
SHAPERSEL
CONFIG_SEL
NOTE: A,B INPUTS ARE SYMMETRIC SO THAT PROTECTION STAMP CARDS CAN BE ROTATED 180 WITHOUT SWAPPING A FOR B.
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DTM2ASD_V6B
TITLE:
DTM2ASD_V6B
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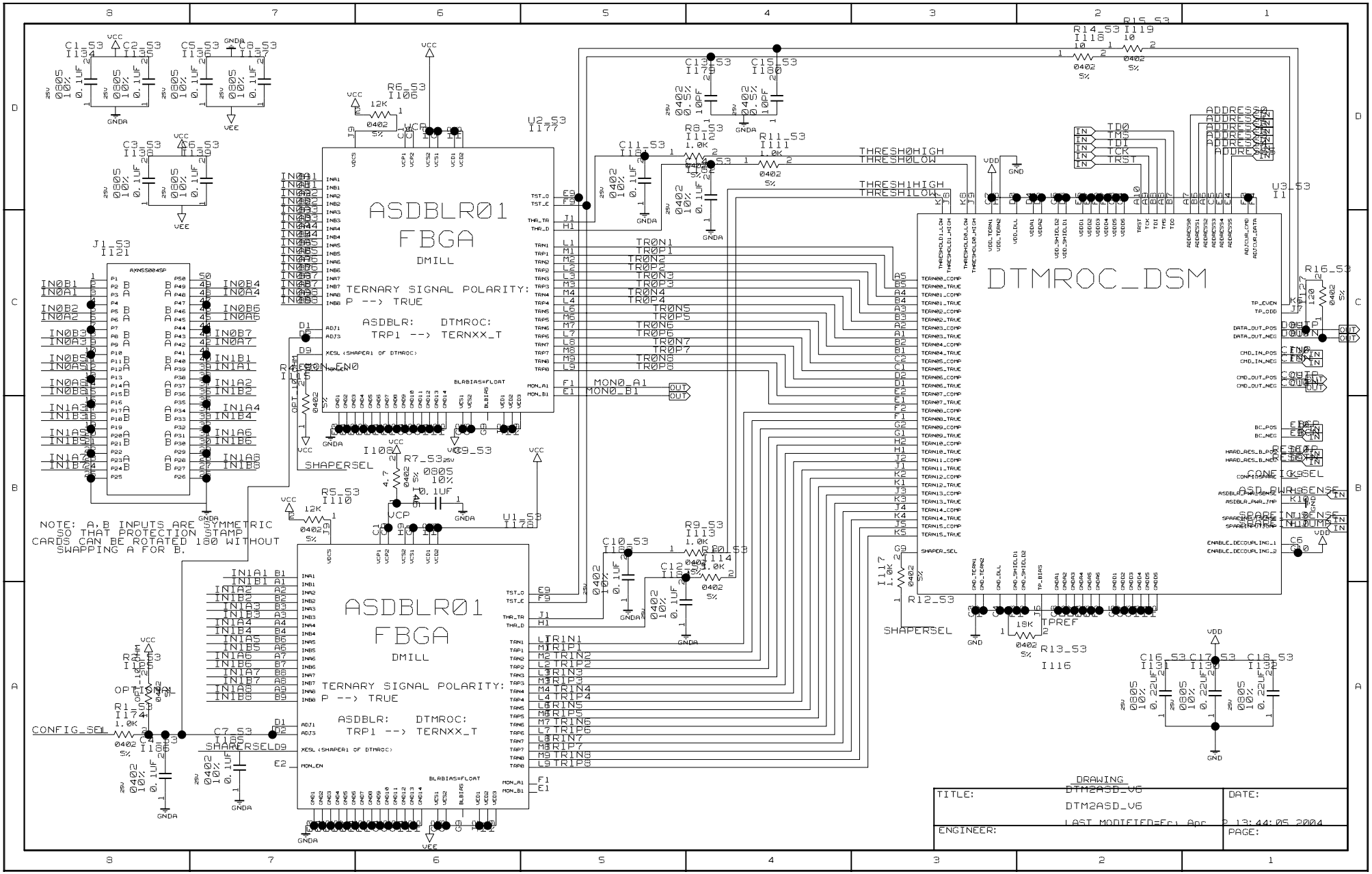
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DTMROC_DSM
SHAPERSEL
CONFIG_SEL
NOTE: A,B INPUTS ARE SYMMETRIC SO THAT PROTECTION STAMP CARDS CAN BE ROTATED 180 WITHOUT SWAPPING A FOR B.
DRAWING
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ENGINEER:
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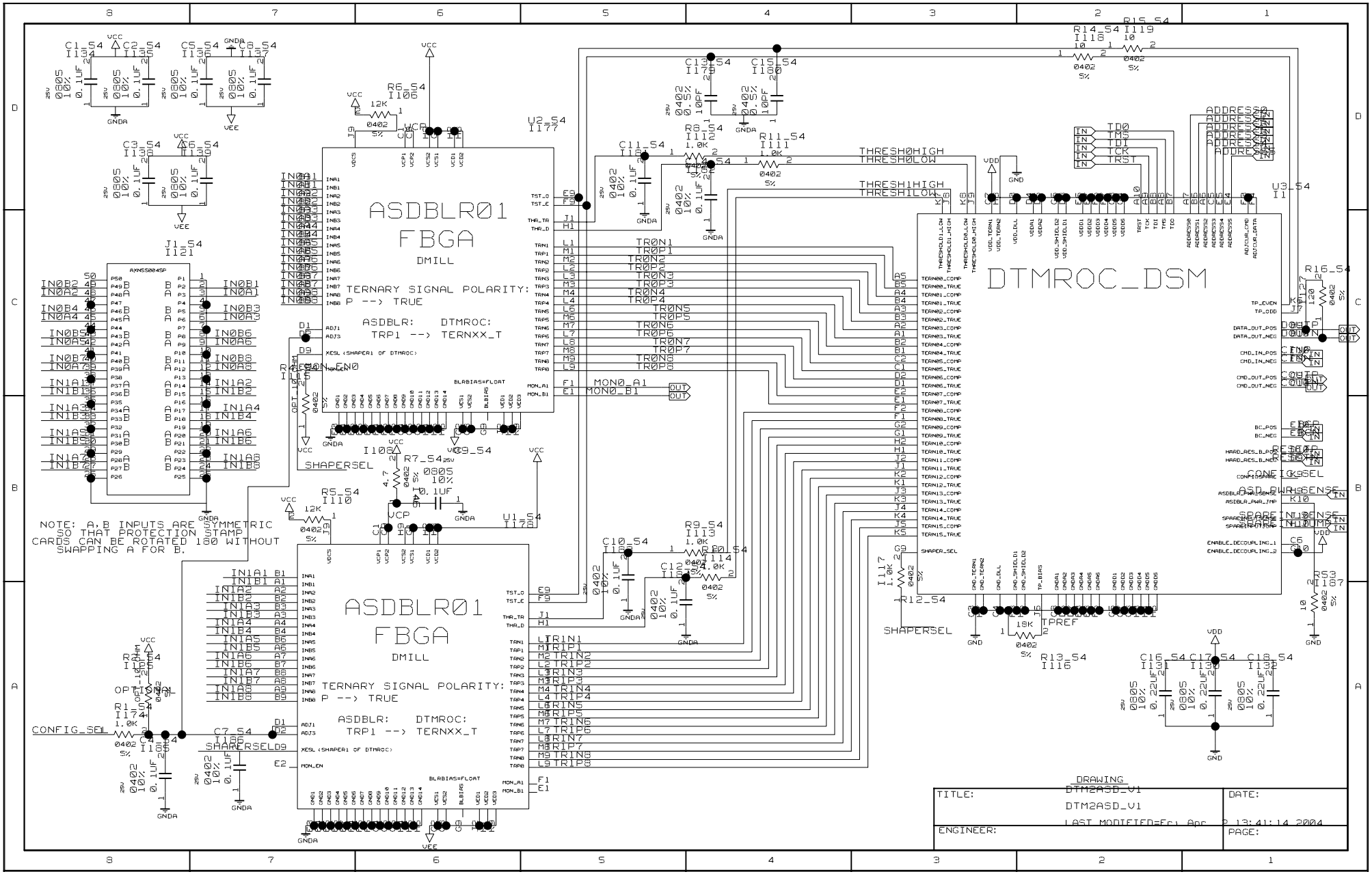
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FBGA
DMILL
TERNARY SIGNAL POLARITY:
P --> TRUE
ASDBLR: DTMROC:
TRP1 --> TERNXX_T
DTMROC_DSM
NOTE: A,B INPUTS ARE SYMMETRIC SO THAT PROTECTION STAMP CARDS CAN BE ROTATED 180 WITHOUT SWAPPING A FOR B.
SHAPERSEL
CONFIG_SEL
DRAWING
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TITLE:
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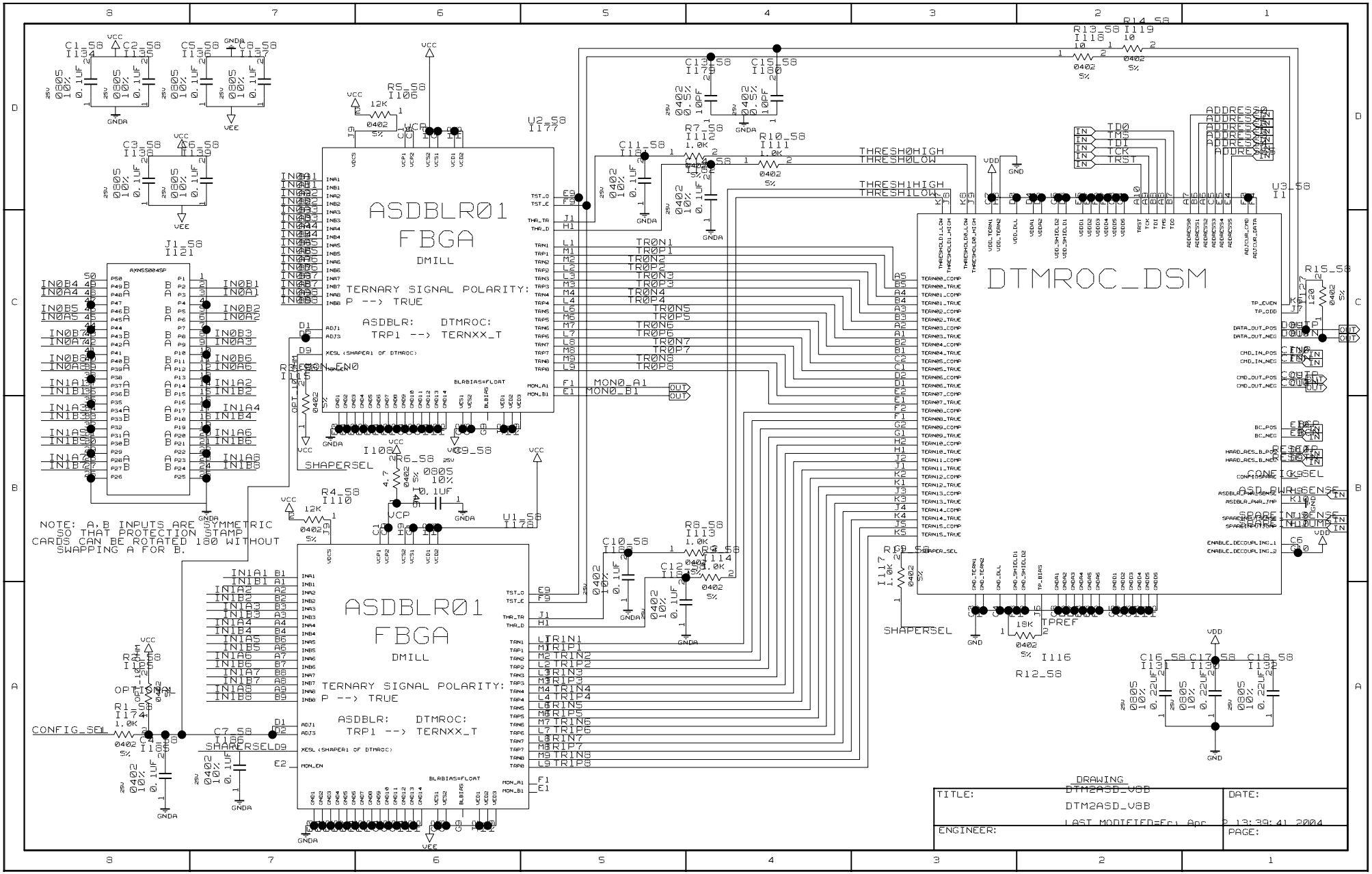
DATE:
ENGINEER:
PAGE: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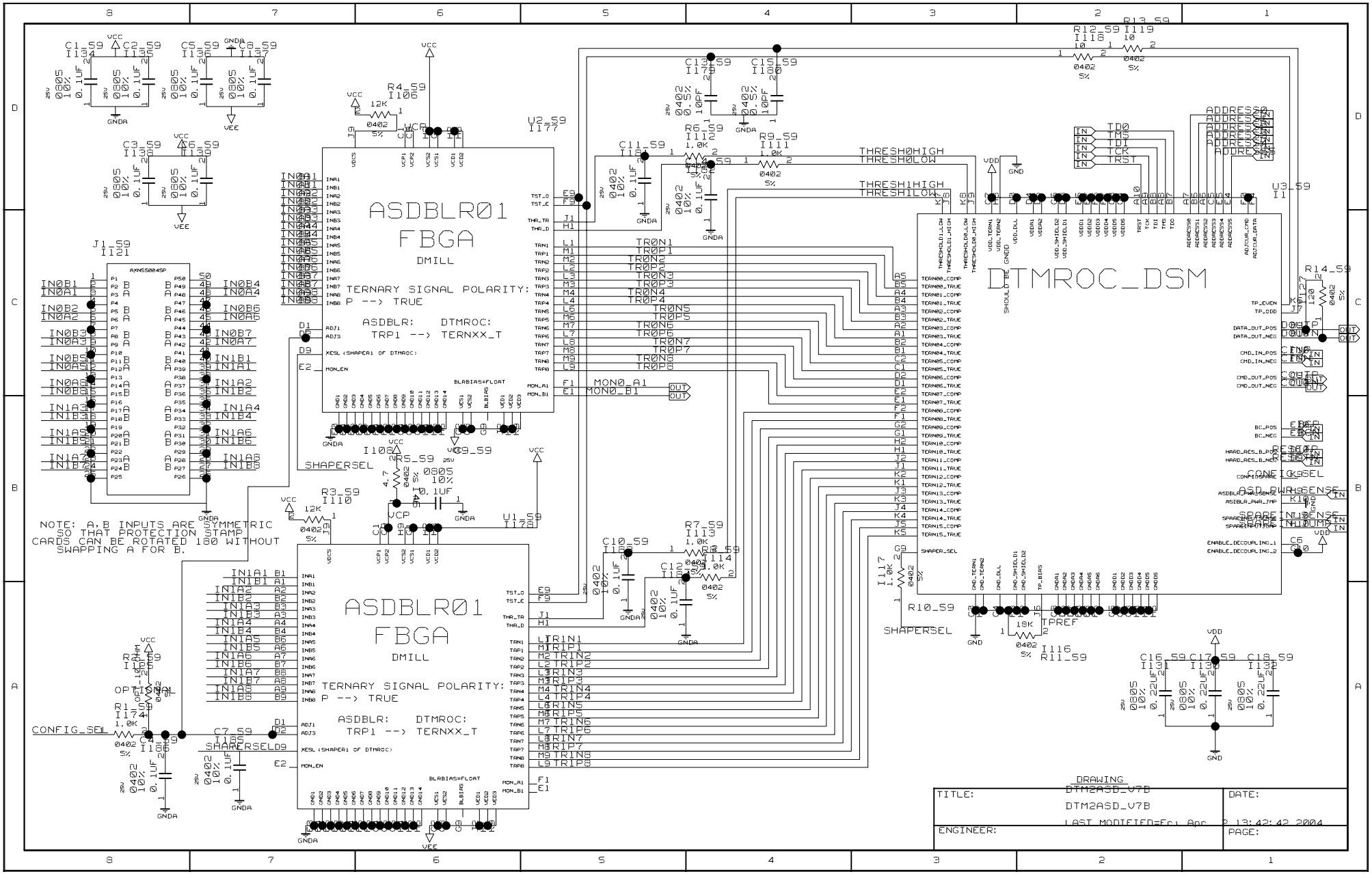
SHAPERSEL
CONFIG_SEL
NOTE: A,B INPUTS ARE SYMMETRIC SO THAT PROTECTION STAMP CARDS CAN BE ROTATED 180 WITHOUT SWAPPING A FOR B.
DRAWING
DTM2ASD_V8B
TITLE:
DTM2ASD_V8B
DATE:
ENGINEER:
PAGE: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NOTE: A,B INPUTS ARE SYMMETRIC SO THAT PROTECTION STAMP CARDS CAN BE ROTATED 180 WITHOUT SWAPPING A FOR B.
SHAPERSEL
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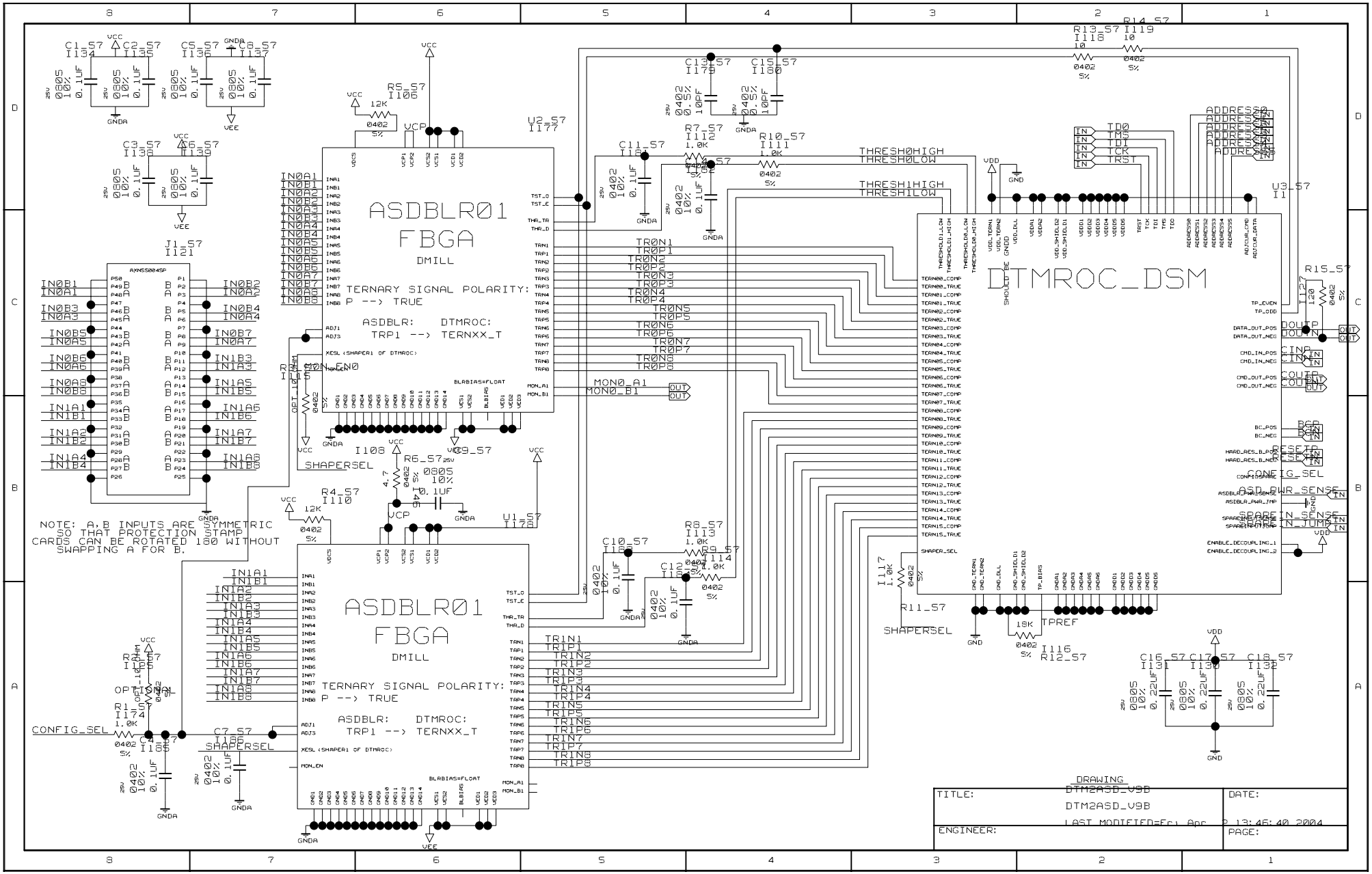
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FBGA
DMILL
TERNARY SIGNAL POLARITY:
P --> TRUE
ASDBLR: DTMROC:
TRP1 --> TERNXX_T
DTMROC_DSM
SHAPERSEL
CONFIG_SEL
NOTE: A,B INPUTS ARE SYMMETRIC SO THAT PROTECTION STAMP CARDS CAN BE ROTATED 180 WITHOUT SWAPPING A FOR B.
DRAWING
DTM2ASD_V9B
TITLE:
DTM2ASD_V9B
DATE:
ENGINEER:
PAGE: