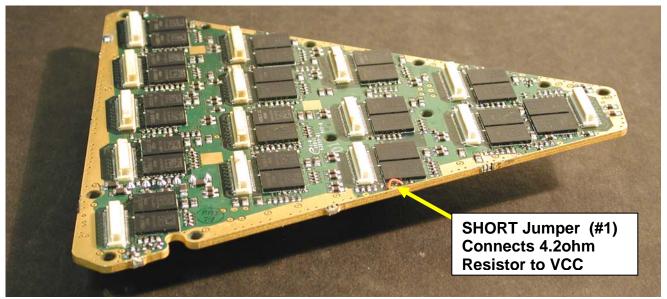
Two Jumpers for the AR2FS board

March 22, 2005

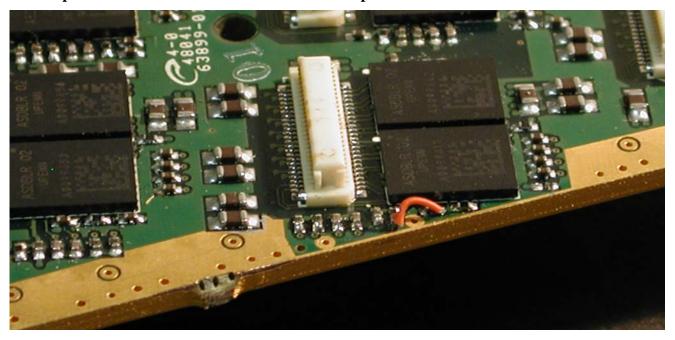
Instructions for the installation of two jumpers to repair bad via contacts at Location #10

Instructions for Jumper #1 (ASDBLR side of board)

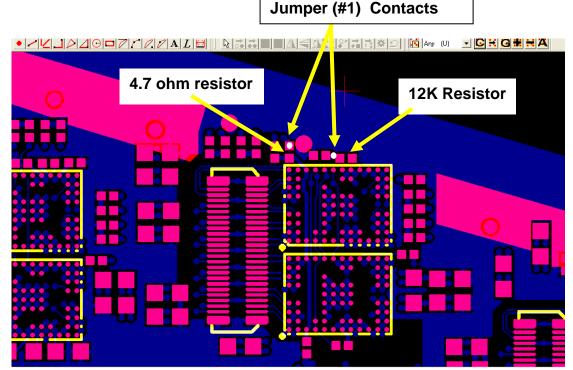
A mistake in the layout of the AR2FS board prevents the 4.70hm preamp resistor at location #10 (DTMROC side silk screen numbering) from connecting to the Vcc supply. This is easily corrected by soldering a jumper to a nearby location. The jumper should connect between a trace that supplies +3V to an 18k resistor and a 0.1uF capacitor (See Gerber View below) and a 4.70hm resistor as shown. It should be rated to carry at least 20mA current. For mechanical reasons 28 or 30AWG wire should be used.



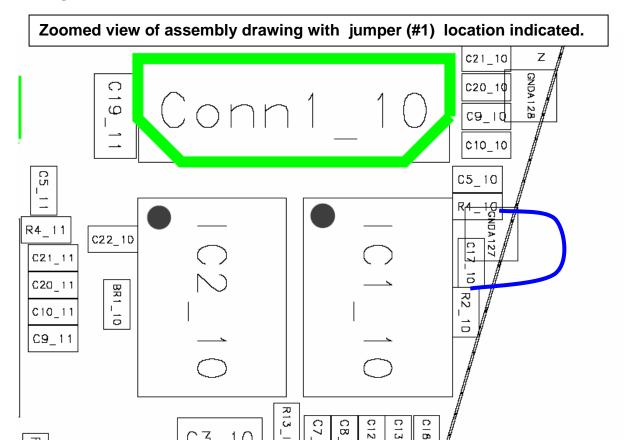
The picture above shows the location of the Jumper on the AR2FS board.



Closeup view of installed jumper. Note jumper attaches to 4.7ohm resistor on contact away from the ASDBLR chip, towards perimeter of board.

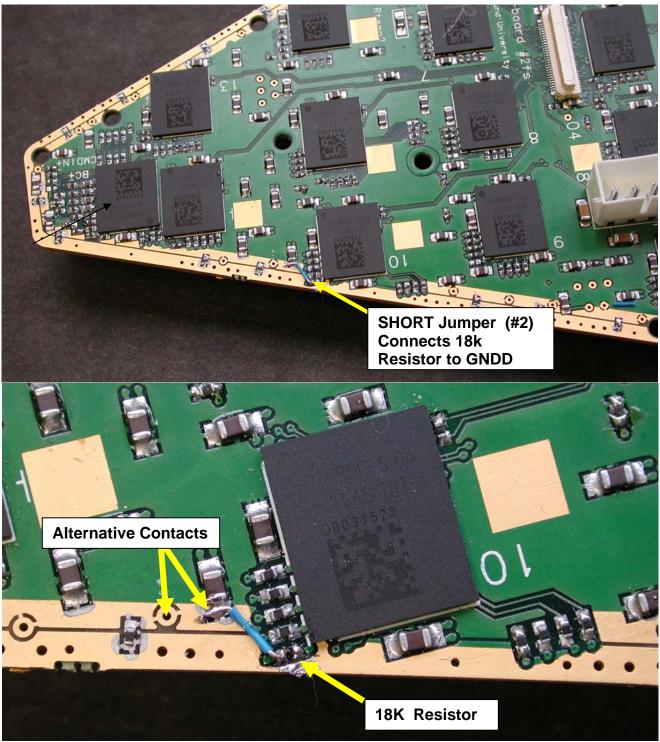


This gerber view with the bottom layer and solder mask "on" shows the jumper contact points. White dots indicate the intended contacts. The junction contact on the right connects to Vcc between a .1uF cap and to a 12K resistor. The junction contact on the left goes to a 4.7ohm resistor.



Instructions for Jumper #2 (also at location #10, DTMROC side)

The 18k ohm resistor indicated below does not connect to digital ground (GNDD) due to a mistake in the via thermal. The jumper indicated will remedy this connection.



The picture above shows two views of the jumper installed at DTMROC location #10. The via to the right offers an alternative connection point for the connection of the jumper to Digital Ground (GNDD).

Zoomed view of assembly drawing with jumper (#2) location indicated. Jumper shown as stuffed. It may connect to the via labeled "GND101" instead of CPD125.

