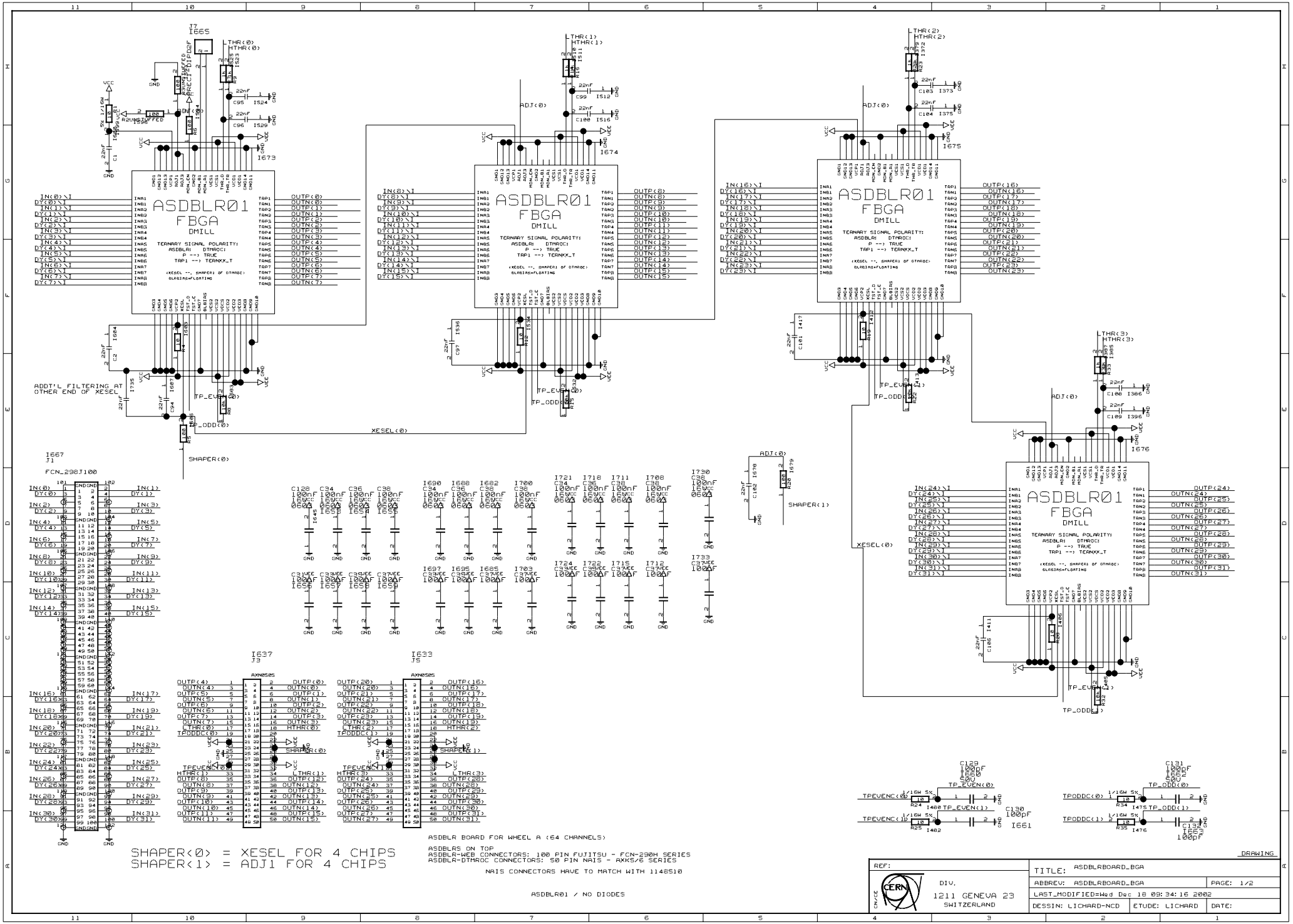
ASDBLR01
FBGA
DMILL
TERNARY SIGNAL POLARITY:
ASDBLR: DTMROC:
P --> TRUE
TRP1 --> TERNXX_T
(XESEL --, SHAPER) OF DTMROC)
BLRBIAS=FLOATING
ADDI'L FILTERING AT OTHER END OF XESEL
XESEL(0)
SHAPER(0)
SHAPER(1)
ADJ(0)
FCN_298J100
SHAPER<0> = XESEL FOR 4 CHIPS
SHAPER<1> = ADJ1 FOR 4 CHIPS
ASDBLR BOARD FOR WHEEL A (64 CHANNELS)
ASDBLRS ON TOP
ASDBLR-WEB CONNECTORS: 100 PIN FUJITSU - FCN-290H SERIES
ASDBLR-DTMROC CONNECTORS: 50 PIN NAIS - AXK5/6 SERIES
NAIS CONNECTORS HAVE TO MATCH WITH 1148510
ASDBLR01 / NO DIODES
DRAWING
REF:
CERN
DIV.
1211 GENEVA 23
SWITZERLAND
TITLE: ASDBLRBOARD_BGA
ABBREV: ASDBLRBOARD_BGA
PAGE: 1/2
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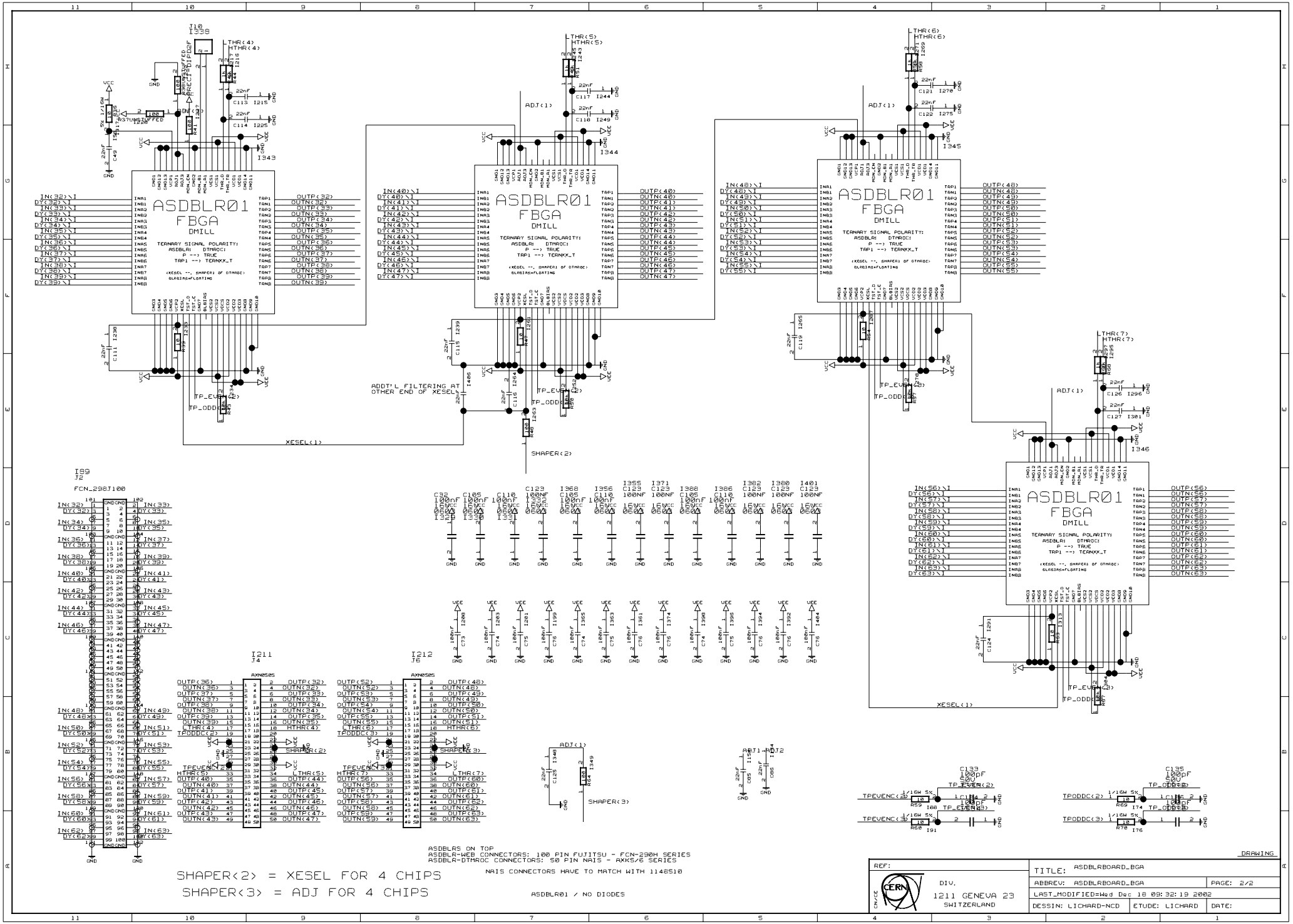
DESSIN: LICHARD-NCD
ETUDE: LICHARD
DATE:


ASDBLR01
FBGA
DMILL
TERNARY SIGNAL POLARITY:
ASDBLR: DTMROC:
P --> TRUE
TRP1 --> TERNXX_T
(XESEL --, SHAPER1 OF DTMROC)
BLRBIAS=FLOATING
ADJ(1)
XESEL(1)
SHAPER(2)
SHAPER(3)
TP_EVEN(2)
TP_ODD(2)
ADDT'L FILTERING AT
OTHER END OF XESEL
I89
J2
FCN_298J100
I211
J4
I212
J3
AXN5S85
ASDBLRS ON TOP
ASDBLR-WEB CONNECTORS: 100 PIN FUJITSU - FCN-290H SERIES
ASDBLR-DTMROC CONNECTORS: 50 PIN NAIS - AXK5/6 SERIES
NAIS CONNECTORS HAVE TO MATCH WITH 1148510
SHAPER<2> = XESEL FOR 4 CHIPS
SHAPER<3> = ADJ FOR 4 CHIPS
ASDBLR01 / NO DIODES
DRAWING
REF:
CERN
DIV.
1211 GENEVA 23
SWITZERLAND
TITLE: ASDBLRBOARD_BGA
ABBREV: ASDBLRBOARD_BGA
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