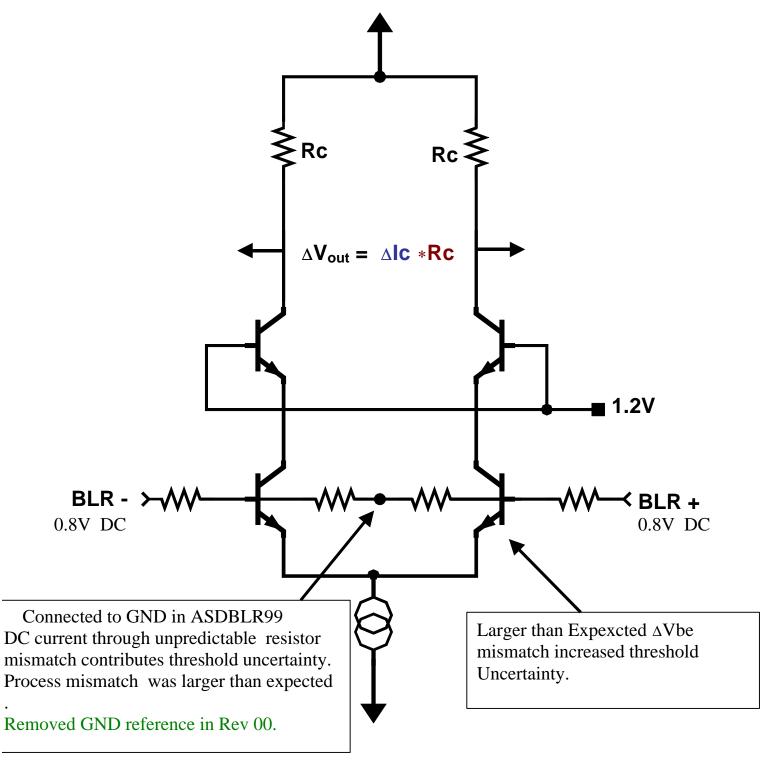# NSS LYON 2000 Atlas TRT Electronics

## Threshold Offset Contributions
### INPUT STAGE TO TRACK DISCRIMINATOR



Rc    Rc

$\Delta V_{out} = \Delta Ic * Rc$

1.2V

BLR -
0.8V DC

BLR +
0.8V DC

Connected to GND in ASDBLR99
DC current through unpredictable resistor
mismatch contributes threshold uncertainty.
Process mismatch was larger than expected
.
Removed GND reference in Rev 00.

Larger than Expexcted $\Delta Vbe$
mismatch increased threshold
Uncertainty.

FMN                                    UPENN