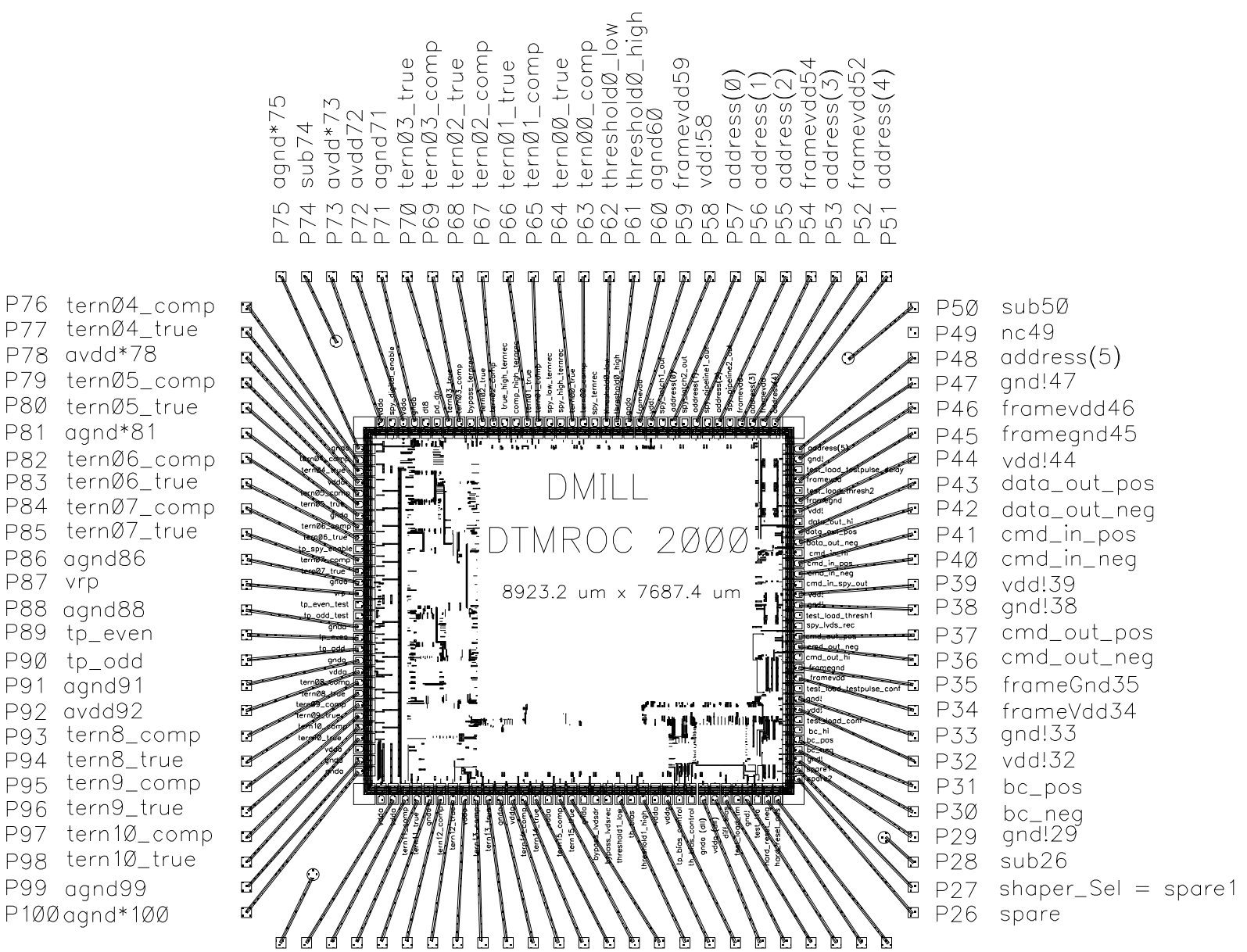


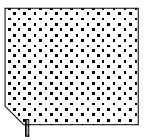
P1 sub1
P2 avdd*2
P3 tern11_comp
P4 tern11_true
P5 agnd*5
P6 tern12_comp
P7 tern12_true
P8 avdd*8
P9 tern13_comp
P10 tern13_true
P11 agnd*11
P12 avdd*12
P13 tern14_comp
P14 tern14_true
P15 tern15_comp
P16 tern15_true
P17 threshold1_low
P18 threshold1_high
P19 avdd*19
P20 gnd20 (DLL)
P21 vdd21 (DLL)
P22 diff_bias
P23 gnd!23
P24 hard_reset_neg
P25 hard_reset_pos

P26 spare
P27 shaper_Sel = spare1
P28 sub26
P29 gnd!29
P30 bc_neg
P31 bc_pos
P32 vdd!32
P33 gnd!33
P34 frameVdd34
P35 frameGnd35
P36 cmd_out_neg
P37 cmd_out_pos
P38 gnd!38
P39 vdd!39
P40 cmd_in_neg
P41 cmd_in_pos
P42 data_out_neg
P43 data_out_pos
P44 vdd!44
P45 framegnd45
P46 framevdd46
P47 gnd!47
P48 address(5)
P49 nc49
P50 sub50

P51 address(4)
P52 framevdd52
P53 address(3)
P54 framevdd54
P55 address(2)
P56 address(1)
P57 address(0)
P58 vdd!58
P59 framevdd59
P60 agnd60
P61 threshold0_high
P62 threshold0_low
P63 tern00_comp
P64 tern00_true
P65 tern01_comp
P66 tern01_true
P67 tern02_comp
P68 tern02_true
P69 tern03_comp
P70 tern03_true
P71 agnd71
P72 avdd72
P73 avdd*73
P74 sub74
P75 agnd*75

P76 tern04_comp
P77 tern04_true
P78 avdd*78
P79 tern05_comp
P80 tern05_true
P81 agnd*81
P82 tern06_comp
P83 tern06_true
P84 tern07_comp
P85 tern07_true
P86 agnd86
P87 vrp
P88 agnd88
P89 tp_even
P90 tp_odd
P91 agnd91
P92 avdd92
P93 tern8_comp
P94 tern8_true
P95 tern9_comp
P96 tern9_true
P97 tern10_comp
P98 tern10_true
P99 agnd99
P100 agnd*100

BONDING DIAGRAM
DTMROC2000 PRODUCTION
SWIRE/IPACK TQFP 100



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